

2/3 FIG. 2

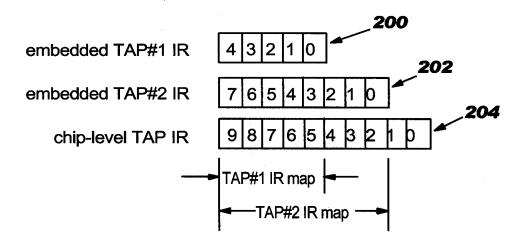


FIG. 3 304 300 **Chip-Level TAP** TAP#1 **TCK** TDI **TDO** 316 310 **TMS** IR9 IR8 IR7 IR6 R5 **320** TAP#2 316 314 **TCK** 312 **TDO** TDI IR1 IR0 IR4 |IR3 |IR2 **TMS** *302* 322 **`320 TAP logic** TCK 4 318 SHIFT-IR **TDI TMS** TAP#1 Enable TRST TAP#2 Enable **TDO**

3/3

